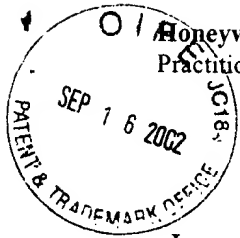


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Honeywell's Docket No. H0001384 (4960)
Practitioner's Docket No. 100665.0026US1

IN THE UNITED STATE PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231

In re application of: **Yutaka DOI**

Serial No: **09/752,660**

Filed: **December 28, 2000**

For: **Layered Circuit Boards and
Methods of Production Thereof**

Group: **2841**

Examiner: **Ishwarbhai B. Patel**

INFORMATION DISCLOSURE STATEMENT

Box DD
Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure imposed by 37 C.F.R. § 1.56 to inform the United States Patent and Trademark Office of all references coming to the attention of the Applicant(s) or attorneys or agents for Applicant(s) which are or may be material to the examination of the subject application, attorneys for the Applicant(s) hereby invite the Examiner's attention to the references listed in the accompanying PTO Form 1449 entitled "List of References Cited".

This submission is understood to complement the results of the Examiner's own independent search. The submission of this Disclosure Statement should not be construed as a representation that a search was made, or that the cited items are inclusive of all relevant and material citations that may be available publicly.

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Honeywell's Docket No. H0001384 (4960)
Practitioner's Docket No. 100665.0025US1

Applicant(s) respectfully request that the Examiner review the foregoing references, as set forth in the Form PTO-1449, and that they be made of record in the file history of the above-captioned application.

Respectfully submitted,

Rutan & Tucker, LLP

Dated: September 9, 2002

By: 

David J. Zoetewey
Reg. No. 45,258

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